



SMD Crystal Oscillator - LVPECL

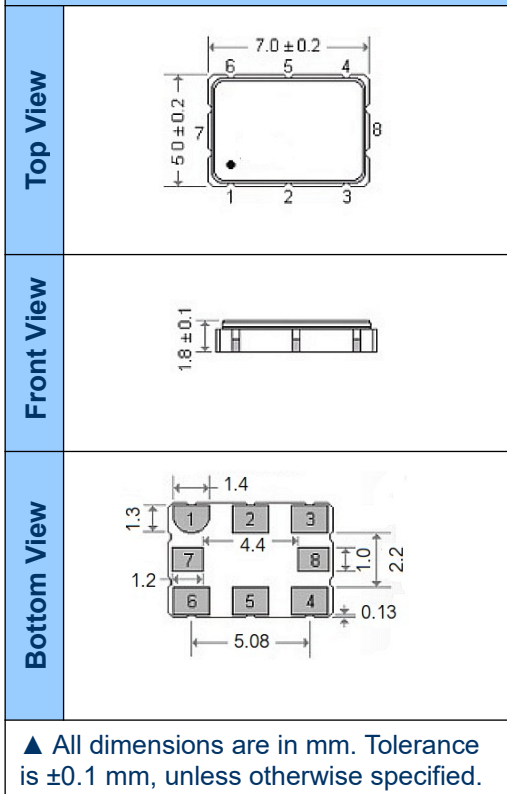
XO 0960

Features

- Ultra-Low Jitter
- LVPECL Output
- RoHS Compliant

Mechanical Specifications

Outline



Parameter	Specifications	
Frequency Range	150 MHz ~ 2100 MHz	
Supply Voltage, V_{dd}	2.5 V \pm 10% (Code: 2) 3.3 V \pm 10% (Code: 3)	
Current Consumption	120 mA (max.)	
Output Waveform	LVPECL	
Load	50 Ω into $V_{dd}-2$ V	
Output High Level	Min.	Max.
	$V_{dd}-1.165$ V	$V_{dd}-0.8$ V
Output Low Level	Min.	Max.
	$V_{dd}-2.0$ V	$V_{dd}-1.55$ V
Duty Cycle	50% \pm 5%	
Rise/ Fall Time	0.5 ns (max.) [20% to 80% Waveform]	
Frequency Stability	Select from Table 1	
Operating Temperature Range (OTR)	Select from Table 1	
V Disable	0.2 V_{dd} (max.)	
V Enable	0.8 V_{dd} (min.)	
Enable Time	2.5 ms (max.)	
Disable Time	10 μ s (max.)	
Start up Time	5 ms (typ.); 10 ms (max.)	
RMS Phase Jitter	155 fs (typ.) [12 KHz ~ 20 MHz]	
Aging @ 25°C	\pm 3 ppm (max.) for First Year; \pm 2 ppm (max.) per Year, thereafter	
Storage Temperature Range	-55°C to +150°C	

Pad Configuration

Pad	Description
1	Output Enable
2	No Connection
3	Ground
4	Output
5	Complimentary Output
6	Supply Voltage
7	No Connection
8	No Connection

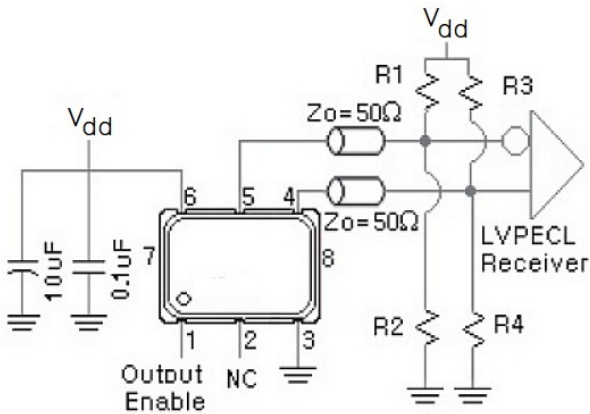
Table 1

OTR Codes	Stability Codes			
	F	G	M	
Frequency Stability		± 20 ppm	± 25 ppm	± 50 ppm
E	-10°C to +70°C			
C	-20°C to +70°C			
I	-40°C to +85°C			

Denotes Available Denotes not Available

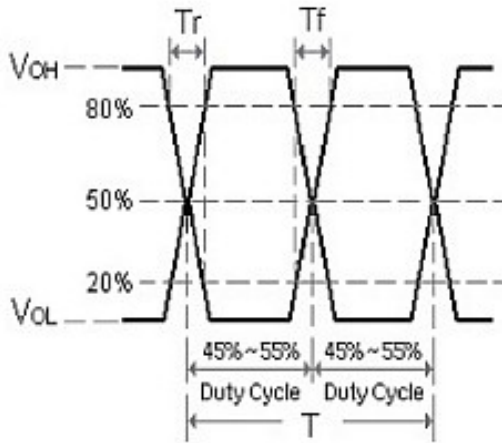
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TEST CIRCUIT



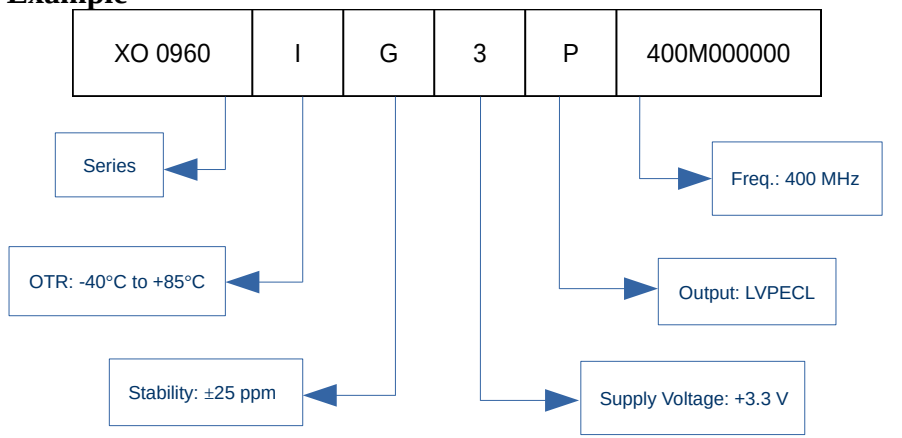
$V_{dd} = 3.3V$: $R1 = R3 = 127 \Omega$; $R2 = R4 = 82.5 \Omega$
 $V_{dd} = 2.5V$: $R1 = R3 = 250 \Omega$; $R2 = R4 = 62.5 \Omega$

Output Waveform



Ordering Information

Example



Specifications subject to change without prior notice

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Note: Not all combination of options are available. Other specifications may be available upon request.

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