



SMD Temperature Compensated Crystal Oscillator

TCXO 7053

Features

- Ceramic SMD Package
- High Frequency Range
- RoHS compliant

Specifications	Option Codes
Frequency Range: 40 MHz ~ 156 MHz	
Output Drive: HCMOS Compatible, Square wave	H
Output Load: 15 pF ± 10%	
Output Level: V _{Low} : 0.1 V _{dd} (max.) V _{High} : 0.9 V _{dd} (min.)	
Rise/ Fall Time: 10 ns (max.)	
Duty Cycle: 45% to 55%	
Start-up Time: 10 ms (max.)	
Frequency Tolerance @ 25°C: ± 2 ppm (max.)	
Operating Temperature Range (OTR) and Temperature Stability: Select from Table 1	
Supply Voltage: +2.5 V ± 5% +3.3 V ± 5%	2 3
Supply Voltage Stability: ±0.3 ppm (max.)	
Load Stability: ±0.3 ppm (max.)	
Current Consumption: 50 mA (max.)	
Frequency Adjustment Option: External Control Voltage (1.5 V ± 1.0 V) None (Fixed Frequency)	V N
Control Voltage Range: 1.5 V ± 1.0 V	
Frequency Tuning Range: ± 5 ppm (min.)	
Frequency Tuning Slope: Positive	
Linearity: 10% (max.)	
Aging @ 25°C: ± 1 ppm (max.) per Year	
Storage Temperature Range: -55°C to +125°C	



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Mechanical Specifications

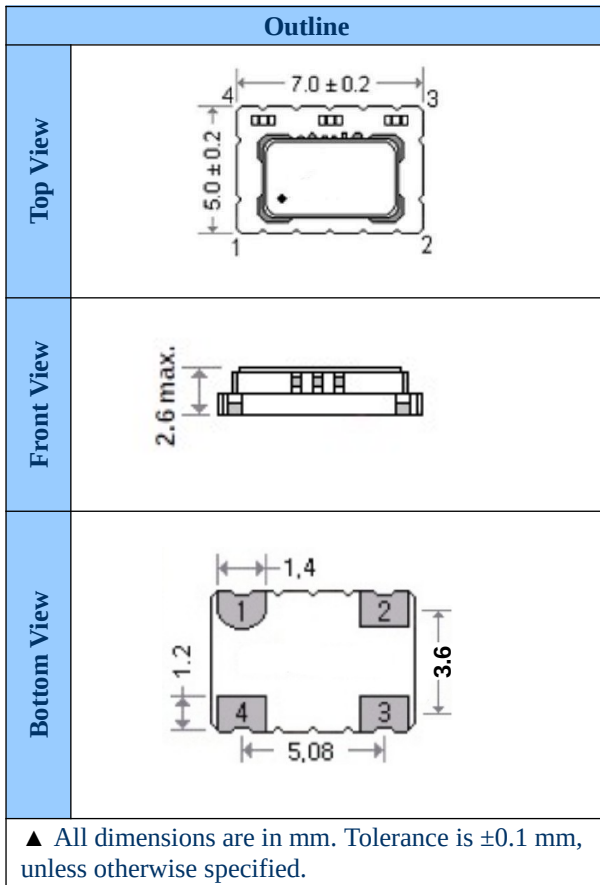


Table 1

OTR Code	Stability Codes				
	Temperature Stability	X ± 0.5 ppm	Y ± 1 ppm	P ± 2 ppm	Q ± 2.5 ppm
A	0°C to +50°C				
D	-10°C to +60°C				
C	-20°C to +70°C				
H	-30°C to +85°C				
I	-40°C to +85°C				

■ Denotes Available □ Denotes not Available

Table 2
Typical SSB Phase Noise @ 77.76 MHz

10 Hz offset	-74 dBc/Hz
100 Hz offset	-99 dBc/Hz
1 KHz offset	-98 dBc/Hz
10 KHz offset	-95 dBc/Hz
100 KHz offset	-90 dBc/Hz

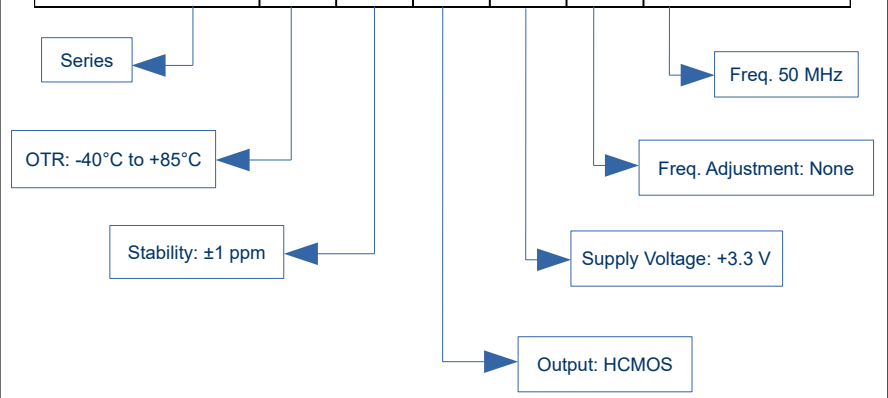
Pad Configuration:

Pad	Option N	Option V
1	No Connection	Control Voltage
2	Ground	Ground
3	Output	Output
4	Supply Voltage	Supply Voltage

Ordering Information

Example

TCXO 7053	I	Y	H	3	N	050M000000
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Specifications subject to change without notice

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Note: Not all combination of options are available. Other specifications may be available upon request.

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